**JUAN SANMARTÍN JARA PH.D., CEng**

Nationality: British

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**PROFILE**

A resourceful and committed **Ph.D. Engineer in Telecommunications** with valuable international experience and knowledge within; Radar, RF, Digital & Analog Electronic Systems under R&D SW & HW projects. Boasts a wealth of exposure within the Automotive, Rail, Transformation and Defence Sectors. An accomplished professional who has achieved their Ph.D. Telecommunications Engineer from the Polytechnic University of Madrid, as well as being an IET Chartered Engineer. An innovative professional, with adept project management skills and proven ability to build and maintain key relationships at all levels; translating requirements and overseeing all issues through to completion. Advanced analytical and decision-making ability compliment excellent communication skills that have been demonstrated in many areas. A life-long learner, who genuinely values opportunities to study, improve and grow in pursuit of positive change: **Now seeking a challenging related role where expertise, experience, and specialist knowledge can be used to bring immediate value.**

**KEY SKILLS AND EXPERTISE**

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| * Telecommunications expertise * Research, analysis and report writing * Facilitation of practical intervention(s) * Consultancy / advisory work * Strategic planning and organisation * Developing new processes and methodologies * Leadership and management * Multi-sector exposure * Time management and organisation * Personal development | * CMMI methodologies * Multi-lingual communications * Strong problem-solving ability * Project management * Qualitative research / Data analysis * Influential ability * Results orientated * Teamwork and administration * Project management * Strong, yet approachable | * The ability to effectively build and manage key relationships |

**PROFESSIONAL EXPERIENCE**

July 2021 – Present **Senior HW & SW Engineer** – Volta Trucks. Winersh. Reading.

**Key Achievements and Responsibilities:**

* **Engineer lead, ensuing compliance with RFQ/RFI, suppliers engagement, deliverables, gateways and milestones.**
* Lead ECU and components supplier analysis, benchmarking and selection.
* Delivering key knowledge and relating with ADAS subsystems, Infotainment, Cockpit User & Comfort Controls, Body Electronics.
* Requirement’s creation and cascaded from the programme to Electrical/Electronic Engineering teams with appropriate required content.
* Ownership with BOM/CAD validation exercises and the tracking of component part releasing.
* Leading Verification & Validation tests on HIL rigs and vehicle.
* Tasked with compliance with: DOORS, JIRA, Confluence, RTC, CAD and EDS interfaces, BOM and parts releasing, Tests & Validation, AIMS (Issues & Problems resolution), EMC, GRADE (Elect. Design Review & Verification), FMA/DFMEA, PACN (Costs & Attributes Control and Assessments), PPAP, PSW, eAPQP

Jul 2020 – May 2021 **EDS & ECU EE Integration Leader** – Pininfarina Automobili, Turin, Italy

**Key Achievements and Responsibilities:**

* **Co-ordinated the supervision of electrical teams, ensuing compliance with deliverables, gateways and milestones for the full Electrical hypersport vehicle.**
* Demonstrated key knowledge and relating with EDS LV and HV wiring for prototypes and Preseries.
* Held full responsibility for Ensuring targets and requirements are cascaded from the programme to Electrical/Electronic Engineering teams with appropriate required content
* Championed directly with BOM/CAD/Requirements validation exercises and the tracking of component part releasing (3DX, Kisters, JAMA)
* Vehicle prototyping integration supporting and problem resolution and other CoC (Powertrain, Body & Trim, Chassis Eng., Launch, Vehicle Eng.) interfacing and coordination, and leading specific investigations and problem resolution.
* Tasked with ECU SW downloading, checking and testing in Automated rigs and in Vehicle. CAN tools used for can tracing, DTC analysis and troubleshooting.

Jul 2014 – May 2020 **ADAS and Controls Lead Engineer** – Jaguar Land Rover, Gaydon

**Key Achievements and Responsibilities:**

* Played a key role as a Project Leader for Rear and Front Corner Radars, Parking sensors, Cameras, Controllers, TPMS, Telematics and Gateway, Chassis ECU system delivery for cross-car lines, ensuring alignment between Electrical/Electronic and Programmes Team, and suppliers to deliver project deliverables.
* Lead ECU and components supplier analysis, benchmarking and selection.
* Challenged with supervising and reporting compliance with company requirements, development, testing, finance and management procedures, DOORS, CAD and EDS interfaces, BOM and parts releasing, RMDV (Tests & Validation), AIMS (Issues & Problems resolution), EMC, GRADE (Elect. Design Review & Verification), FMA/DFMEA, PACN (Costs & Attributes Control and Assessments), PPAP, PSW, eAPQP
* Leading Requirements Tests definitions, SW Tests plan and DVP specifications .
* **Championed the supervision of teams of up to 30 individuals in company and supplier, ensuing compliance with deliverables, gateways and milestones**
* Demonstrated key knowledge and relating with Infotainment (Audio & Video, Dig. & Analog Signals), Body Electronics, Comfort & Control Electronics, Chassis Electronics, Power Supply Systems and ADAS
* Vehicle integration supporting and issues resolution. ECU SW versions downloading and testing in Automated rigs and in Vehicle. CAN tools used for can tracing, DTC analysis and troubleshooting.
* Knowledgeable and detail analysis in Electronic schematics, low level software, networks circuitry, Ethernet and Flexray connected ECU.

Oct 2011 – Jun 2014 **Electrical Project Leader** – Jaguar Land Rover, Coventry, Whitley, Gaydon

**Key Achievements and Responsibilities:**

* **Championed the supervision of electrical teams of up to 20 individuals, ensuing compliance with deliverables, gateways and milestones**
* Demonstrated key knowledge and relating with Driver Information, Infotainment (Audio & Video, Dig. & Analog Signals), Body Electronics, Comfort & Control Electronics, Chassis Electronics, Power Supply Systems
* Held full responsibility for Ensuring targets and requirements are cascaded from the programme to Electrical/Electronic Engineering teams with appropriate required content
* Co-ordinated directly with BOM/CAD validation exercises and the tracking of component part releasing
* Tasked with supporting consistent project reviews and senior reviews to a set format publication of programme status sheet, updates of project management tracking tools as required

Feb 2006 – Jun 2011 **Technology & Business Development Engineer** – SIDSA, Madrid

**Key Achievements and Responsibilities:**

* Played a key role in identifying, evaluating and managing business opportunities and technology development in European, Middle East and North African markets for Digital Television (DVB), Mobile TV (DVB-H) Internet Protocol TV (IPTV) and Conditional Access (CAS) systems
* Tasked with negotiating and managing joint software and hardware development projects between European and American companies in MPEG-2/4/H264 Coding-Transcoding, IP-encapsulation, IP multiplexing, (Generic PC platform, Firmware: C language for ARM architecture, System-level software: C++ for generic PC architecture (Windows and Linux OS supported), Application level software: Java for all stand-alone user application. Java under Eclipse framework
* Applied in-house Semiconductors & SoC/IC’s designs and championed the management of technical teams using methodologies & planning tools for controlling schedules, budgets, tasks, testing and delivering during development
* Held full responsibility for managing and controlling international R&D (HW & SW) projects under CELTIC (EU commission) & ESA budgeted 1 M€ to 2,4 M€ for developing DTT, DVB-H, CAS, IPTV vertical applications.
* Spearheaded collaborative and technical leadership in advanced development of Satellite Flat Antennas (X & Ku band) and the RF front-end (semiconductors & IC’s) between companies and Universities. CST, ADS, Altium, Orcad.
* Maintained active participation and contribution in International Forums, Congresses and Fairs: Digital Video Broadcasting (DVB), Broadcast Mobile Convergence (BMCO), Mobile World Congress (MWC), International Broadcasting Conference (IBC), Mobile TV Middle East, CeBIT, CABSAT, North America Broadcast (NAB)

Dec 2004 – Jan 2006 **Research & Development Professional** – Global Consulting Touch, Madrid

**Key Achievements and Responsibilities:**

* Challenged with managing new business and technologies products development and implementation for localization and cellular communication areas
* Played a key role in managing technical teams and utilising CMMI procedures and methodologies, planning tools for controlling schedules, budget and costs, tasks, performance metrics and recovery actions during development
* Oversaw the latest localisation technologies application HW & Embedded SW in Assisted-GPS, E-OTD, UTDOA, Galileo project, GSM/GPRS, UMTS devices in transportation and personal use
* Managed Joint software and hardware development agreements between American and European companies such as GSM/GPRS/UMTS (Sagem, Enfora) y GPS (SiRF, Global Locate)
* Maintained full technical responsibility in European Commission projects GR-POSTER (Galileo Ready Positioning Terminal)

Jul 2002 – Nov 2004 **Engineering Director** – TELSA

**Key Achievements and Responsibilities:**

* Tasked with managing engineering department staff to provide complete Ethernet networks for data & phone communications and control solutions
* Played a key role in performing tendering, development and delivery for implementation of Voice & Data communications for motorways, railroads, private corporations in Europe, South America projects budgeted more than 1,5M€
* Completed collaborative technology agreements with first-rank companies, such as Allied Telesys, Enterasys (LAN, WAN, WLAN), Mitsubishi Electric Co, (Power Line Comm.) and Avaya (structured cabling)
* Championed engineering staff to provide cost-efficient technical solutions in LAN, WAN WLAN, VoIP, PDH & SDH, Radio Links, Tolling & Industrial control, CCTV and Structured Cabling

**PERSONAL DEVELOPMENT**

**Ph. D. Telecommunications Engineer. Mention Cum Laude**

**Doctoral Thesis:** “Contribution to the Spread Spectrum Techniques Applied to Low Probability of Interception Radar Systems”. Escuela Técnica Superior de Ingeniería de Telecomunicaciones. Universidad Politécnica de Madrid, Madrid 1996 – 2002. (Superior Engineering in Telecommunications School. Polytechnic University of Madrid)

**Telecommunications Engineer:** Escuela Técnica Superior de Ingeniería de Telecomunicaciones. Universidad Politécnica de Madrid, Madrid 1987. (Superior Engineering in Telecommunications School. Polytechnic University of Madrid)

**Professional Registration:** The IET Chartered Engineer (September 2013). MIET

**Other Qualifications;** Understanding Quantum Computing. Keio University

**ADDITIONAL INFORMATION**

**Languages:** English (fluent), Italian (Intermadiate), German (Basic) and Spanish (Native)

**Full Clean Driving Licence**

**Interest:** Cycling, walking, history, reading and music, motorcycles, watches.

**REFERENCES AVAILABLE UPON REQUEST**